

JOINT SPECIAL ISSUE

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on

GaAs IC's

This special issue will comprise 10 to 15 papers of special caliber and technical interest as selected from those submitted to and presented at the *1981 GaAs IC Conference* to be held in *San Diego, California, last week of October 1981*. The general areas of interest will be:

- Linear Monolithic Low Noise, Small-Signal or Power Circuits
- Digital Monolithic Integrated Circuits
- Device Physics, Modeling and Simulation
- Materials Advances
- Processing Technology
- Radiation and Reliability
- Packaging and Testing

Selection of papers will be made by the Technical Program Committee of the GaAs IC Conference. Deadline for final manuscripts will be January 15 1982. All manuscripts should conform to standard format as indicated in the "Information to IEEE Authors" found in any Transaction. Papers submitted for this *Special Issue* are subject to the normal review procedures and must clearly describe what new and significant results have been obtained.

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